REDUCED COMPONENT POWER CONVERTER WITH INDEPENDENT REGULATED OUTPUTS AND METHOD

IN THE CLAIMS

Please amend the claims as follows.

- 1. (Currently Amended) A power converter comprising:
- a shared first-side stage to receive an input;
- a plurality of second-side converter stages coupled to the first-side stage, each of secondside converter stages to generate an output;

control circuitry to separately monitor the outputs of the second-side eonverter stages and generate a control signal for each output, wherein the control signal turns off switching elements of a corresponding one of the second-side converter stage to regulate the output; and

steering eireuitry diodes in series with coupling switching elements of the first-side stage coupling the switching elements of the first-side stage to switching elements of the second_side converter stages to allow forward bias current to flow from the switching elements of the firstside stage to the switching elements of the second-side eonverter stages and to inhibit current from flowing in a reverse bias direction between the switching elements of the second-side eonverter stages when a switching element of one of the second-side eonverter stages is turned off before a switching element of one of the other second-side eonverter stages; and

a switching signal generator comprising a plurality of sets of transforming windings, wherein switching signals for switching on and off each of the switching elements of the first-side stage are each provided by separate windings of the sets of transforming windings, and wherein center tap windings of the sets of transforming windings provides a switching signal for switching on and off one of the switching elements of the second-side stages.

2. (Currently Amended) The power converter of claim 1 further comprising: a switching signal generator to generate a switching signal for switching on and off the switching elements of the first-side stage, and switching on and off the switching elements of the plurality of second side converter stages; and

a plurality of second-side driver circuits, each to provide one of the second-side converter stages with a combined signal corresponding with the switching signal and one of the control

DO NOT ENTER 10-1-06